DVM32U2T8/16G

16GB - 288-Pin 2Rx8 Unbuffered Non-ECC DDR4 DIMM

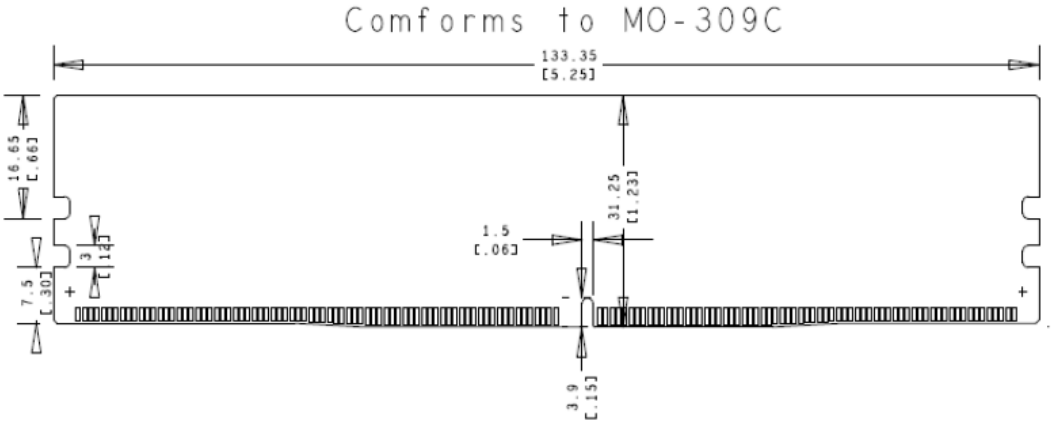
Identification
DVM32U2T8/16G 2Gx64
16GB 2Rx8 PC4-3200AA-U22

Performance Range
Clock / Module Speed / CL-t_{RCD} –t_{RP}
1600 MHz / PC4-3200 / 22-22-22
1467 MHz / PC4-2933 / 21-21-21
1333 MHz / PC4-2666 / 19-19-19
1200 MHz / PC4-2400 / 18-18-18
1200 MHz / PC4-2400 / 17-17-17
1067 MHz / PC4-2133 / 16-16-16
1067 MHz / PC4-2133 / 15-15-15
933 MHz / PC4-1866 / 14-14-14
933 MHz / PC4-1866 / 13-13-13
800 MHz / PC4-1600 / 12-12-12
800 MHz / PC4-1600 / 11-11-11
667 MHz / PC4-1600 / 10-10-10

Features	Description
288-pin JEDEC-compliant DIMM, 133.35 mm wide by 31.25 mm high Operating Voltage: VDD/VDDQ = 1.2V (1.14V to 1.26V) Operating Temperature (Environment, Ambient T_{OPR}) – 0 to 65 C Operating Temperature (DRAM T_{OPER}) – 0 to 85 C VPP = 2.5V (2.375V to 2.75V) VDDSPD = 2.25V to 2.75V I/O Type: 1.2 V signaling Data Transfer Rate: 25.6 Gigabytes/sec Data Bursts: 8 and burst chop 4 mode ZQ Calibration for Output Driver and On-Die Termination (ODT) Programmable ODT / Dynamic ODT during Writes Programmable CAS Latency: 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 21, 22 Bi-directional Differential Data Strobe signals Per DRAM Addressability is supported Write CRC is supported at all speed grades DBI (Data Bus Inversion) is supported(x8 only) CA parity (Command/Address Parity) mode is supported 16 internal banks SDRAM Addressing (Row/Col/BG/BA): 16/10/2/2 Fully RoHS Compliant	**DVM32U2T8/16G** is an Unbuffered 2Gx64 memory module, which conforms to JEDEC's DDR4-3200, PC4-3200AA standard. The assembly is Dual-Rank. Each rank is comprised of eight 1Gbx8 DDR4-3200 SDRAMs. One EEPROM is used for Serial Presence Detect. Both output driver strength and input termination impedance are programmable to maintain signal integrity on the I/O signals in a Fly-by topology.

Notes

Tolerances on all dimensions except where otherwise indicated are ±.13 (.005).
All dimensions are expressed in millimeters [inches]



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