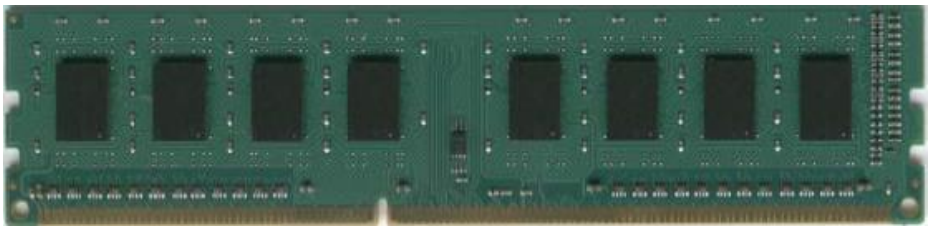
DTI16U1L8W/4G

4GB - 240-Pin 1Rx8 Unbuffered Non-ECC LV DDR3 DIMM Industrial Temp

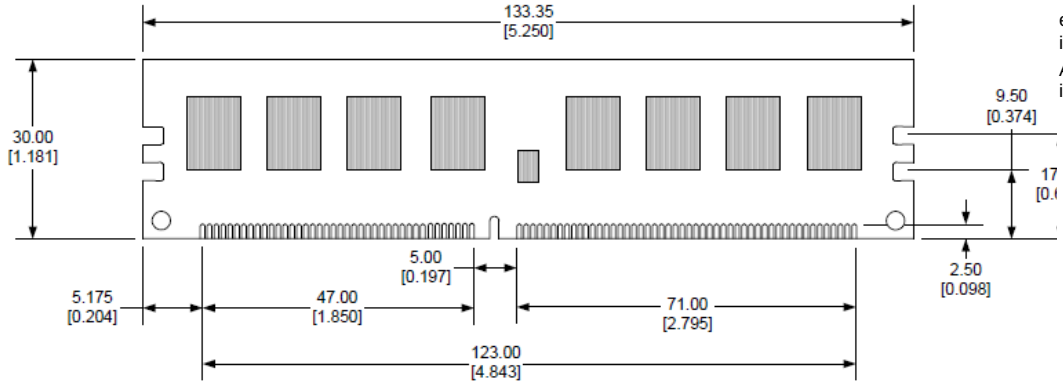
Identification
DTI16U1L8W/4G 512Mx64
4GB 1Rx8 PC3L-12800U-11 WT



Performance Range
Clock / Module Speed / CL-t_{RCD} –t_{RP}
800 MHz / PC3-12800 / 11-11-11
667 MHz / PC3-10600 / 10-10-10
667 MHz / PC3-10600 / 9-9-9
533 MHz / PC3-8500 / 8-8-8
533 MHz / PC3-8500 / 7-7-7
400 MHz / PC3-6400 / 6-6-6

Features	Description
240-pin JEDEC-compliant DIMM, 133.35 mm wide by 30.00 mm high Operating Voltage: VDD = VDDQ = +1.35V (1.283V to 1.45V) Backward-compatible to VDD = VDDQ = +1.5V ±0.0075 V I/O Type: SSTL_15 Operating Temperature Range (-40°C~ 95°C) - tREFI 7.8us at -40 °C ≤ TCASE ≤ 85°C - tREFI 3.9us at -40 °C ≤ TCASE ≤ 95°C Data Transfer Rate: 12.8 Gigabytes/sec Data Bursts: 8 and burst chop 4 mode ZQ Calibration for Output Driver and On-Die Termination (ODT) Programmable ODT / Dynamic ODT during Writes Programmable CAS Latency: 6, 7, 8, 9, 10 and 11 Differential Data Strobe signals SDRAM Addressing (Row/Col/BG/BA): 16/10/3 Fully RoHS Compliant	**DTI16U1L8W/4G** is an unbuffered 512Mx64 memory module, which conforms to JEDEC's DDR3L, PC3L-12800 standard. The assembly is Single Rank. The rank is comprised of eight 512Mx8 DDR3 Industrial Temp SDRAMs. One EEPROM is used for Serial Presence Detect. Both output driver strength and input termination impedance are programmable to maintain signal integrity on the I/O signals in a Fly-by topology.

Front view



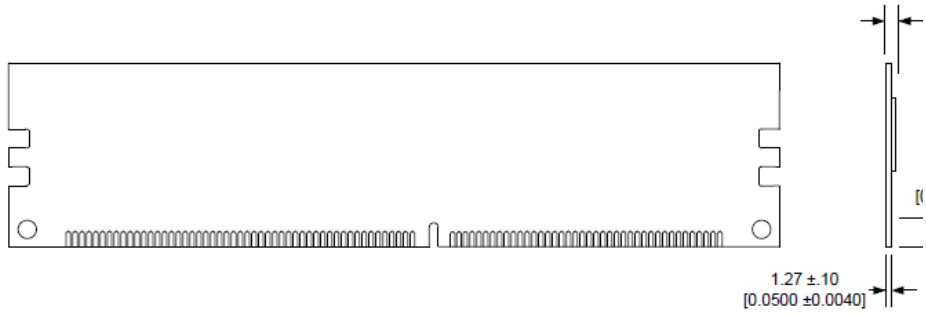
Notes

Tolerances on all dimensions except where otherwise indicated are ±.13 (.005).

All dimensions are expressed in millimeters [inches]

Back view

Side vie



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